## CLAIMS:

1. An image display device comprising:

a matrix display panel having plural X

direction signal lines and plural Y direction signal

lines intersecting lying at right angles thereto, the

intersecting points of said matrix being pixels of a

display image;

X direction driving means for sequentially scanning said plural X direction signal lines to provide image signals;

Y direction driving means for driving said plural Y signal lines in synchronism with the scanning of said plural X direction signal lines to sequentially provide, select signals to said plural Y direction signal lines;

A-D converter means for receiving an analog image signal and converting it into a digital image signal;

voltage generating means for generating signals at plural different voltage levels; and

selector means for selecting one of the output signals from said voltage generating means in accordance with an output from said A-D converter means to provide it to said X direction driving means as said image signal.

2. An image display device according to claim 1, wherein said matrix display panel comprises a liquid crystal display panel.

- An image display device according to claim 1, wherein said matrix display panel comprises a plasma display panel.
- An image display device according to claim 2, wherein said liquid crystal panel comprises liquid crystal cells capable of distinguishably displaying information of N (N: positive integer) for one pixel, and said voltage generating means generates signals at different 2N kinds of voltage levels.
- wherein said liquid crystal panel comprises display elements radiating N (N: positive integer) kinds of colors for one pixel, said X direction signal lines comprises 2N signal lines expresponding to said N kinds of different colors, and said A-D converter means comprises means for receiving N kinds of different color image analog signals and converting them into N kinds of different color image digital signals.
- An image display device according to claim 1, wherein said A-D converter means converts said analog input signal into an M (M: positive integer) bit parallel digital signal to be supplied to said selector means.